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APPLICANTS

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** CONTINUING DATA *****
P.D.
NONE

** FOREIGN APPLICATIONS *****
P.D.
YES
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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <i>Seong-wook Kim</i> Examiner's Signature	STATE OR COUNTRY KOREA, REPUBLIC OF	SHEETS DRAWING 2	TOTAL CLAIMS 6	INDEPENDENT CLAIMS 2
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TITLE

Method for fabricating merged logic CMOS device

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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